

IN THE SPECIFICATION:

Please amend paragraph 0012 as follows:

[0012] FIG. 2A is a block diagram of an embodiment of a receiver clock circuit having a clock duty cycle adjuster circuit.

Please amend paragraph 0014 as follows:

[0014] FIG. 2C is a block diagram of an alternate embodiment of a receiver clock circuit having a clock duty cycle adjuster circuit and a feed back loop.